

AF/2822

PATENT  
ATTORNEY DOCKET NO. 040879-0009-07

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                                  |   |                      |
|----------------------------------|---|----------------------|
| In re Application of:            | ) |                      |
|                                  | ) |                      |
| Stanford W. CRANE, Jr. et al.    | ) |                      |
|                                  | ) |                      |
| Application No.: 09/964,542      | ) | Group Art Unit: 2827 |
|                                  | ) |                      |
| Filed: September 28, 2001        | ) | Examiner: J. Norris  |
|                                  | ) |                      |
| For: PREFABRICATED SEMICONDUCTOR | ) |                      |
| CHIP CARRIER                     | ) |                      |

**MAIL STOP AF**

Commissioner for Patents  
U.S. Patent and Trademark Office  
2011 South Clark Place  
Customer Window  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Sir:

**AMENDMENT TRANSMITTAL FORM**

1. Transmitted herewith is an Amendment responding to the final Office Action dated **March 12, 2003**.
2. Additional papers enclosed:

- |                          |  |
|--------------------------|--|
| <input type="checkbox"/> | Drawings: <input type="checkbox"/> Formal <input type="checkbox"/> Informal (Correction)   |
| <input type="checkbox"/> | Information Disclosure Statement   |
| <input type="checkbox"/> | Form PTO-1449, _____ references included   |
| <input type="checkbox"/> | Citations  |
| <input type="checkbox"/> | Declaration of Biological Deposit  |
| <input type="checkbox"/> | Submission of "Sequence Listing", computer readable copy and/or amendment pertaining thereto for biotechnology invention containing nucleotide and/or amino acid sequence. |

3. Extension of Time

The proceedings herein are for a patent application and the provisions of 37 C.F.R. § 1.136(a) apply.

- ☐ Applicant believes that no extension of time is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition and fee for extension of time.
- ☐ Applicant petitions for an extension of time, the fees for which are set out in 37 C.F.R. § 1.17(a), for the total number of months checked below:

| <u>Total Months Requested</u>         | <u>Fee for Extension</u> | <u>[Fee for Small Entity]</u> |
|---------------------------------------|--------------------------|-------------------------------|
| <input type="checkbox"/> one month    | \$ 110.00                | \$ 55.00                      |
| <input type="checkbox"/> two months   | \$ 410.00                | \$ 205.00                     |
| <input type="checkbox"/> three months | \$ 930.00                | \$ 465.00                     |
| <input type="checkbox"/> four months  | \$ 1,450.00              | \$ 725.00                     |

Extension of time fee due with this request: \$\_\_\_\_\_.

If an additional extension of time is required, please consider this a Petition therefor.

- ☐ An extension for \_\_\_\_\_ months has already been secured and the fee paid therefor of \$\_\_\_\_\_ is deducted from the total fee due for the total months of extension now requested.

4. Constructive Petition

- ☒ EXCEPT for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account 50-0310. This paragraph is intended to be a CONSTRUCTIVE PETITION FOR EXTENSION OF TIME in accordance with 37 C.F.R. § 1.136(a)(3).

5. Fee Calculation (37 C.F.R. §1.16)

| CLAIMS AS AMENDED                                     |   |       |                                |                  |              |            |
|---|---|-------|--------------------------------|------------------|--------------|------------|
|   | Claims<br>Remaining<br>After<br>Amendment |       | Highest No.<br>Previously Paid | Present<br>Extra | at Rate of   | Total Fees |
| Total Claims<br>(37 C.F.R. §1.16(c))                  | 9   | minus | 20                             | 0                | x \$18 each= | + \$ 0     |
| Independent Claims<br>(37 C.F.R. §1.16(b))            | 3   | minus | 3                              | 0                | x \$84 each= | + \$ 0     |
| [ ] First presentation of Multiple dependent claim(s) |   |       |                                |                  | \$280.00     | + \$ 0     |
| SUB-TOTAL =   |   |       |                                |                  |              | \$ 0       |
| Reduction by ½ for filing by a small entity           |   |       |                                |                  |              | - \$ 0     |
| TOTAL FEE =   |   |       |                                |                  |              | \$ 0.00    |

6. Fee Payment

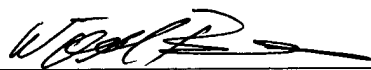
- ☒ No fee is to be paid at this time.
- ☐ The Commissioner is hereby authorized to charge Deposit Account No. 50-0310 in the amount of \$\_\_\_\_\_ representing \_\_\_\_\_.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, including fees due under 37 C.F.R. §§ 1.16 and 1.17, or credit any overpayment to Deposit Account 50-0310.

Respectfully submitted,

**MORGAN, LEWIS & BOCKIUS LLP**

Dated: June 12, 2003

By:

  
William G. Battista, Jr.  
Reg. No. 37,525

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Sir:

**RESPONSE**

In response to the final Office action of March 12, 2003, Applicant respectfully requests entry of this response and reconsideration of the subject application in view of the following amendments and remarks. The period for response to this Office Action continues to run through June 12, 2003.

**In the claims:**

Please replace claims 47-55 with the following rewritten claims 47-55:

47. (Amended) A semiconductor die carrier comprising:  
a housing for holding at least one semiconductor die and including:  
an end surface having a perimeter;  
a peripheral side wall connected to the end surface, extending about the perimeter  
of the end surface and including an outer peripheral surface and an inner peripheral  
surface; and